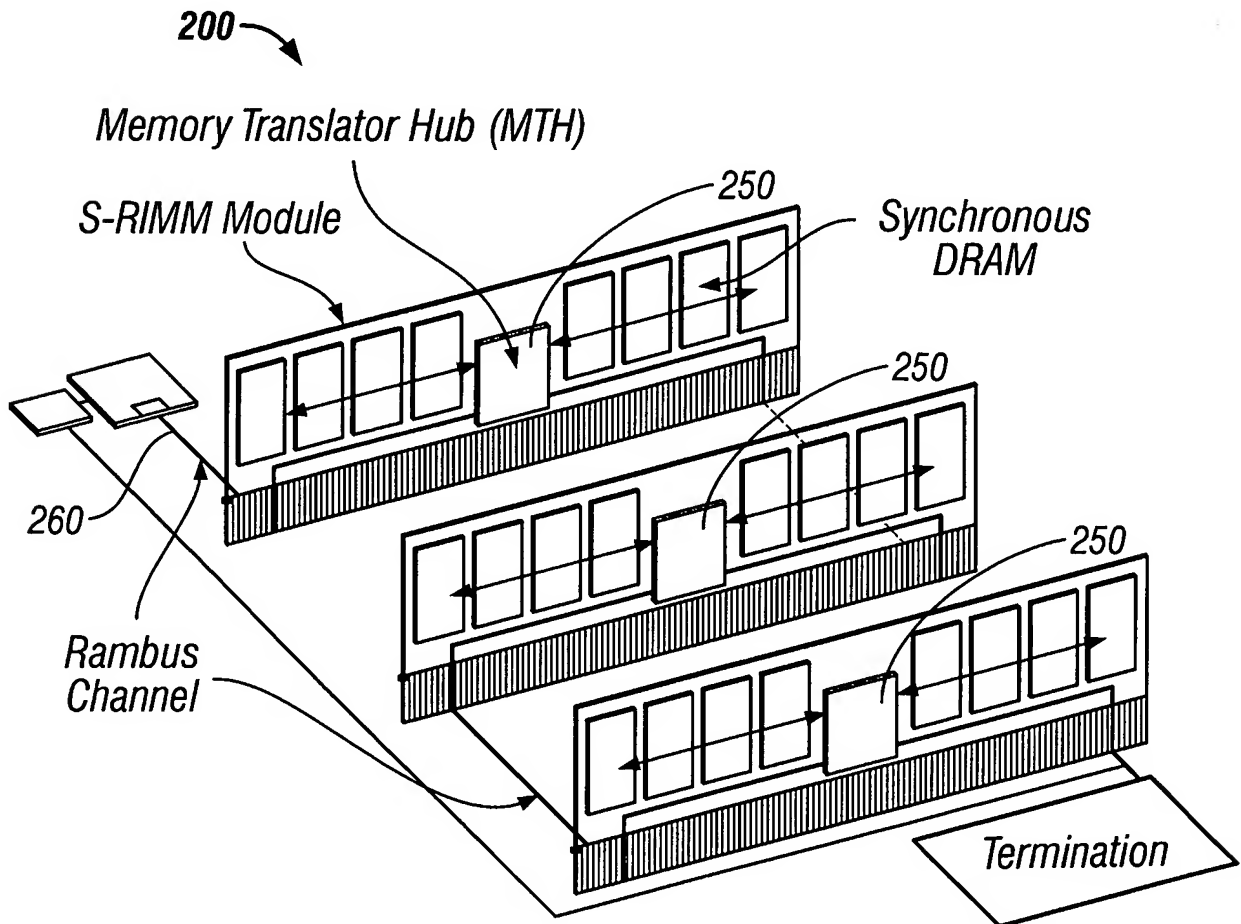


**FIG. 1**  
**(Prior Art)**



**FIG. 2A**  
**(Prior Art)**

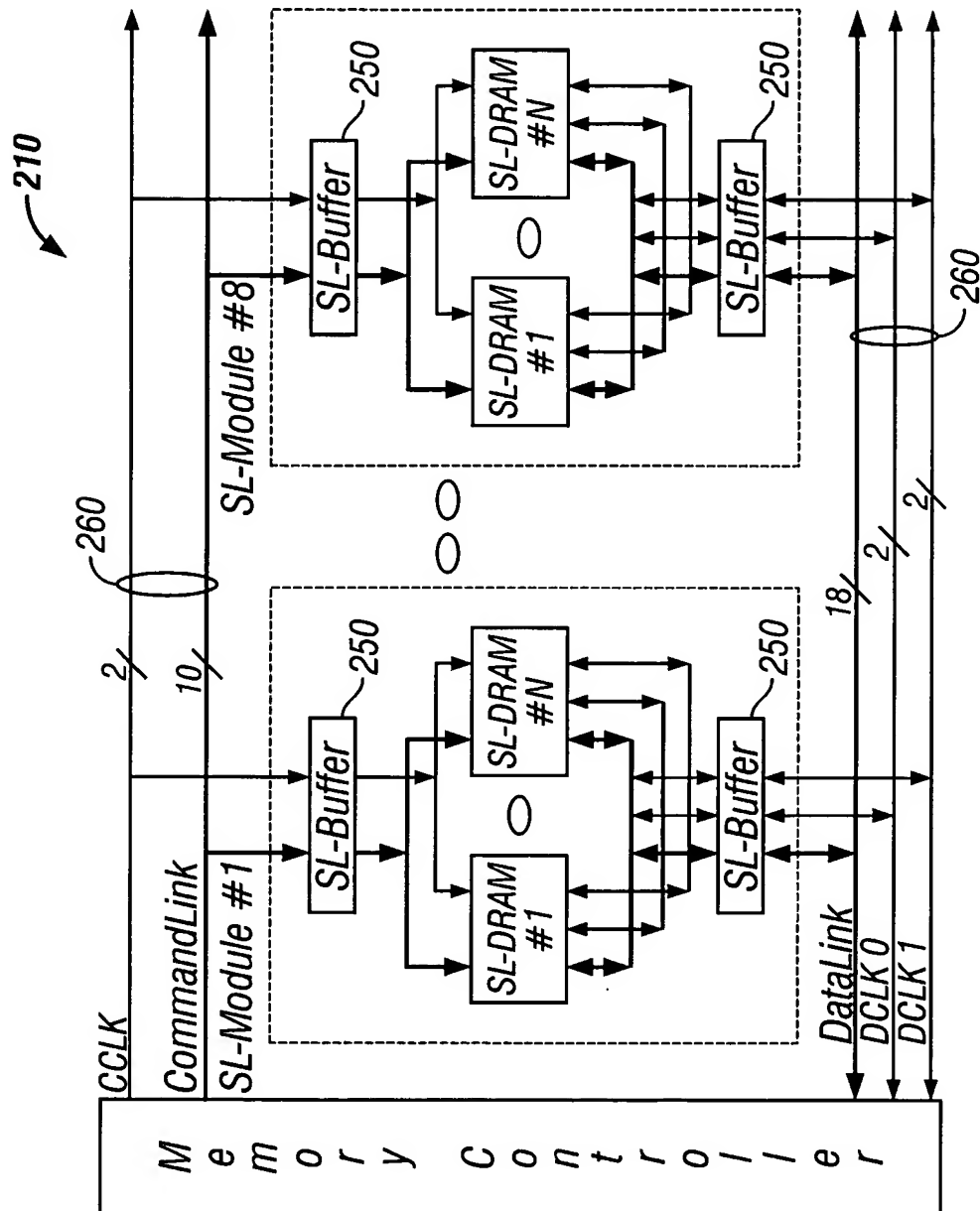


FIG. 2B  
 (Prior Art)

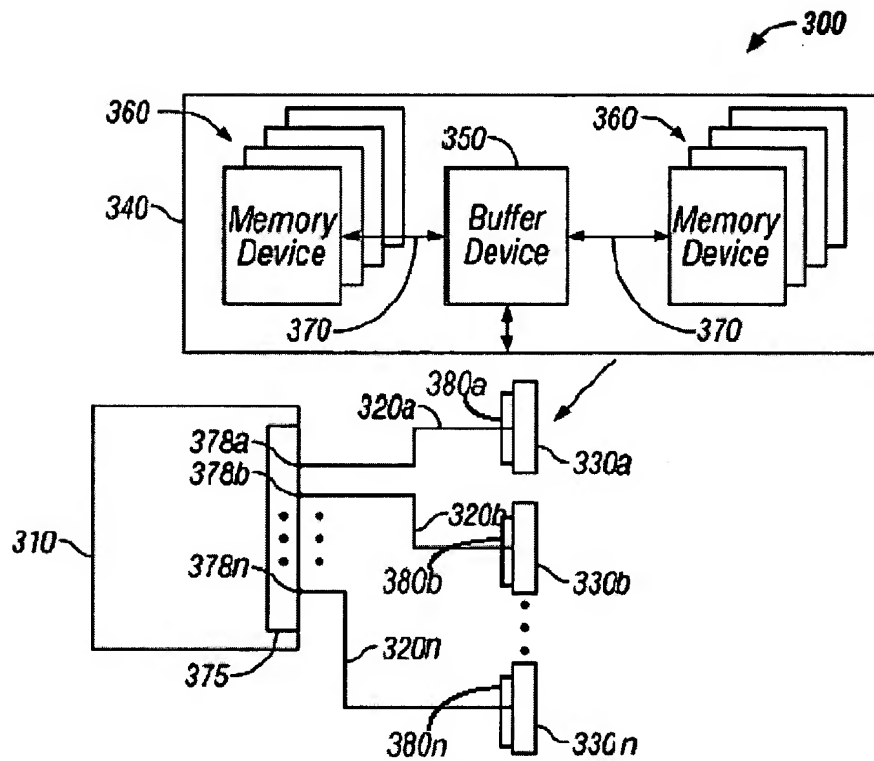


FIG. 3A

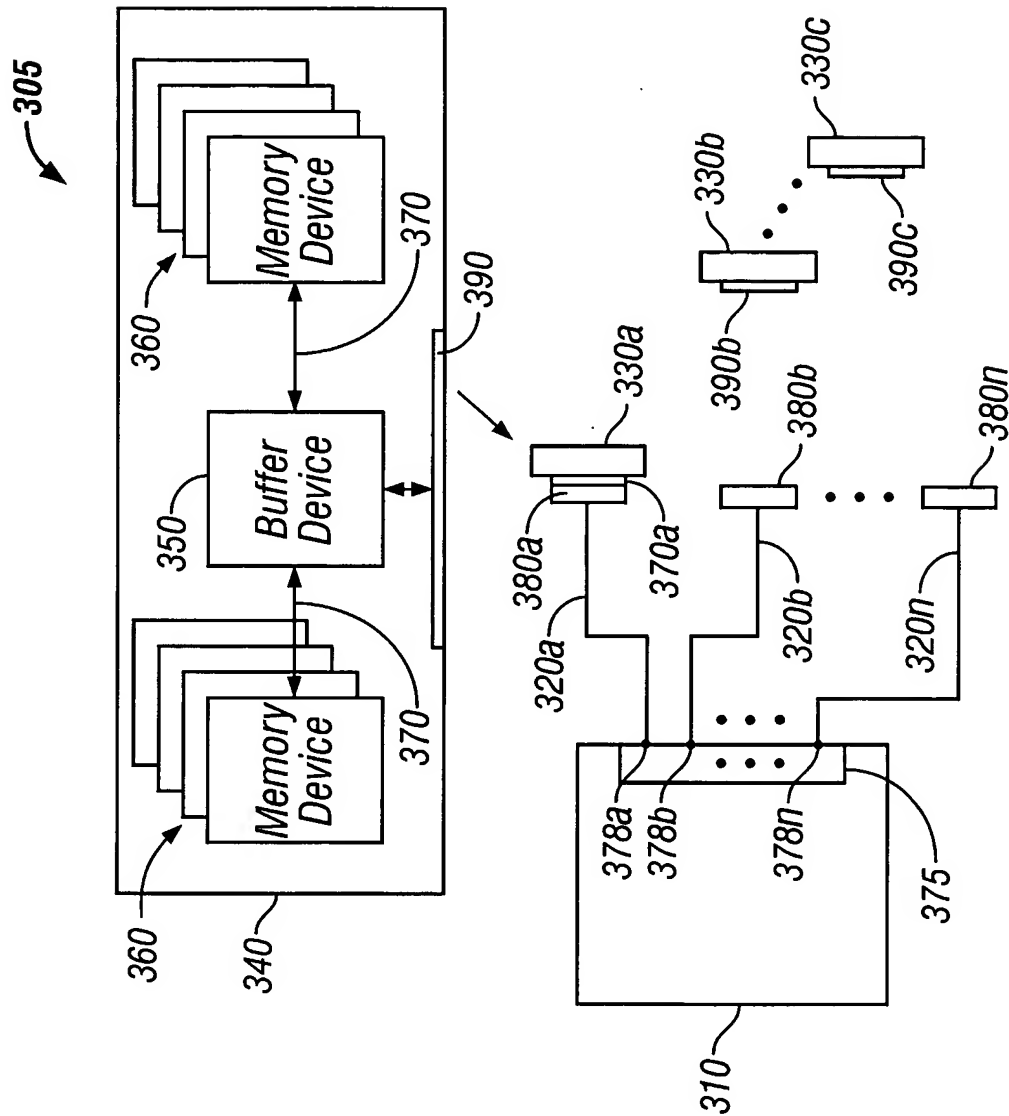
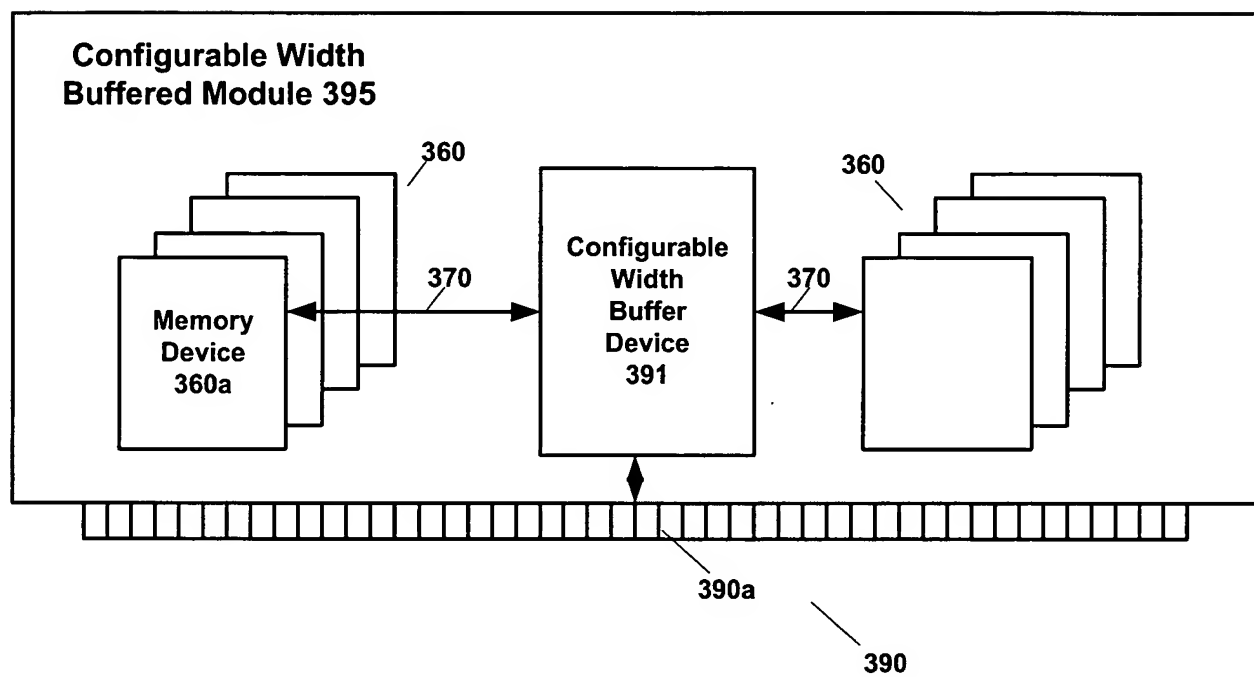


FIG. 3B

**Fig. 3C**

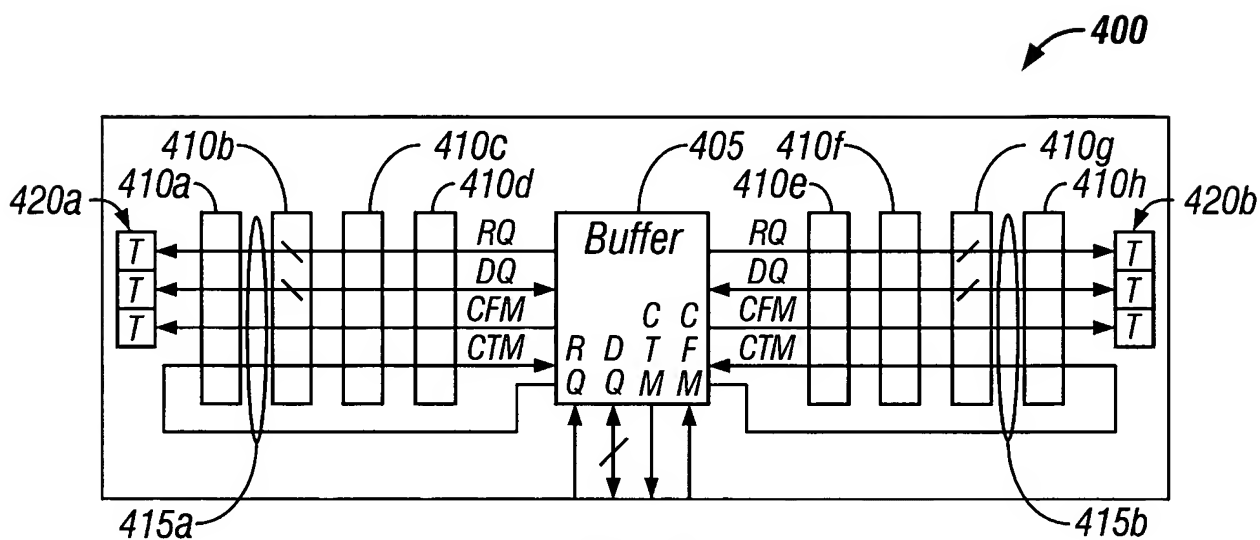


FIG. 4A

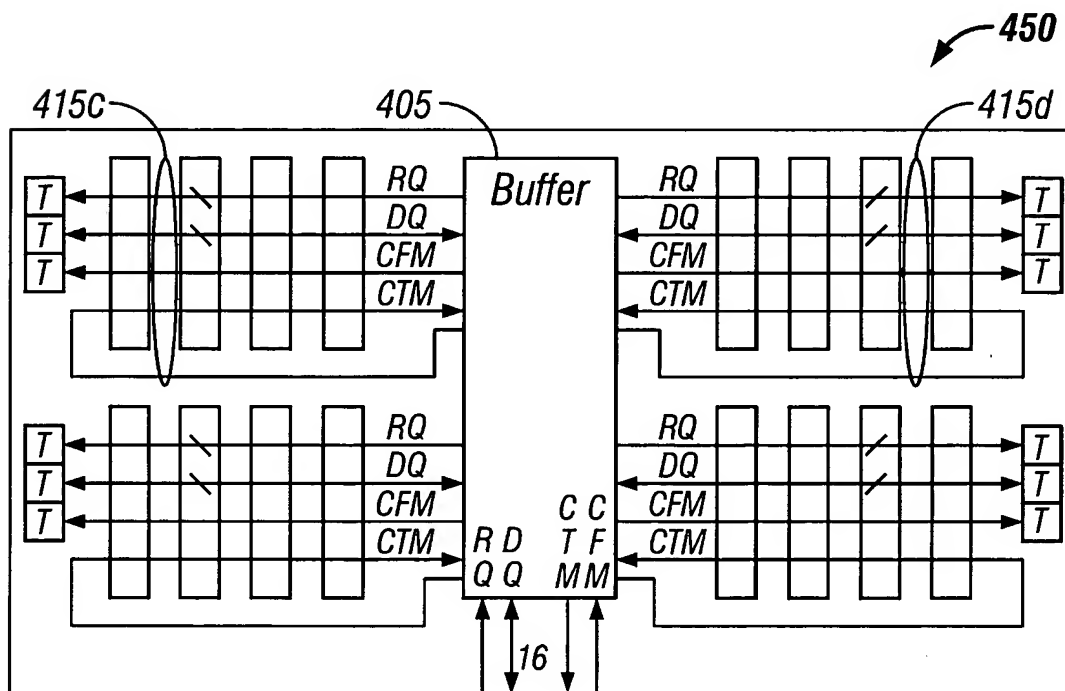
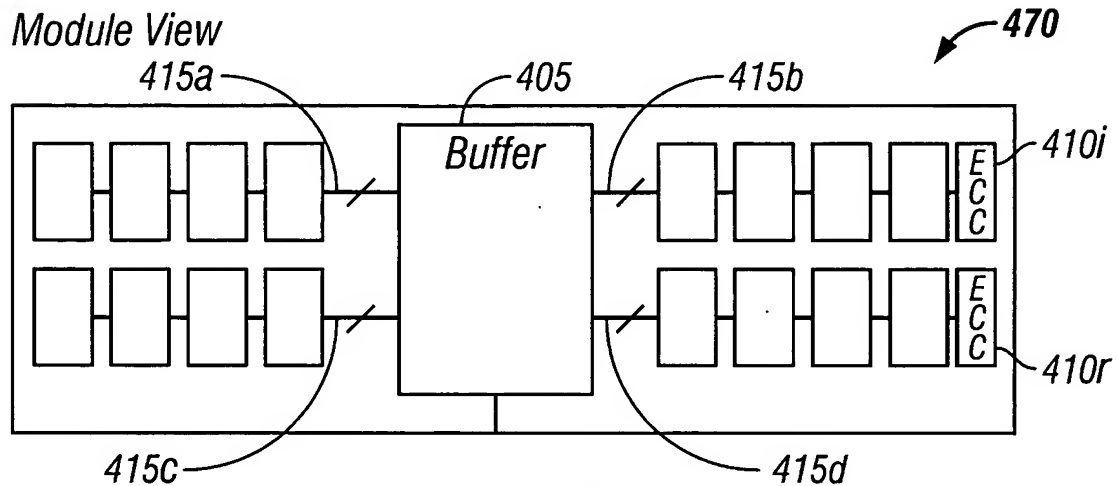
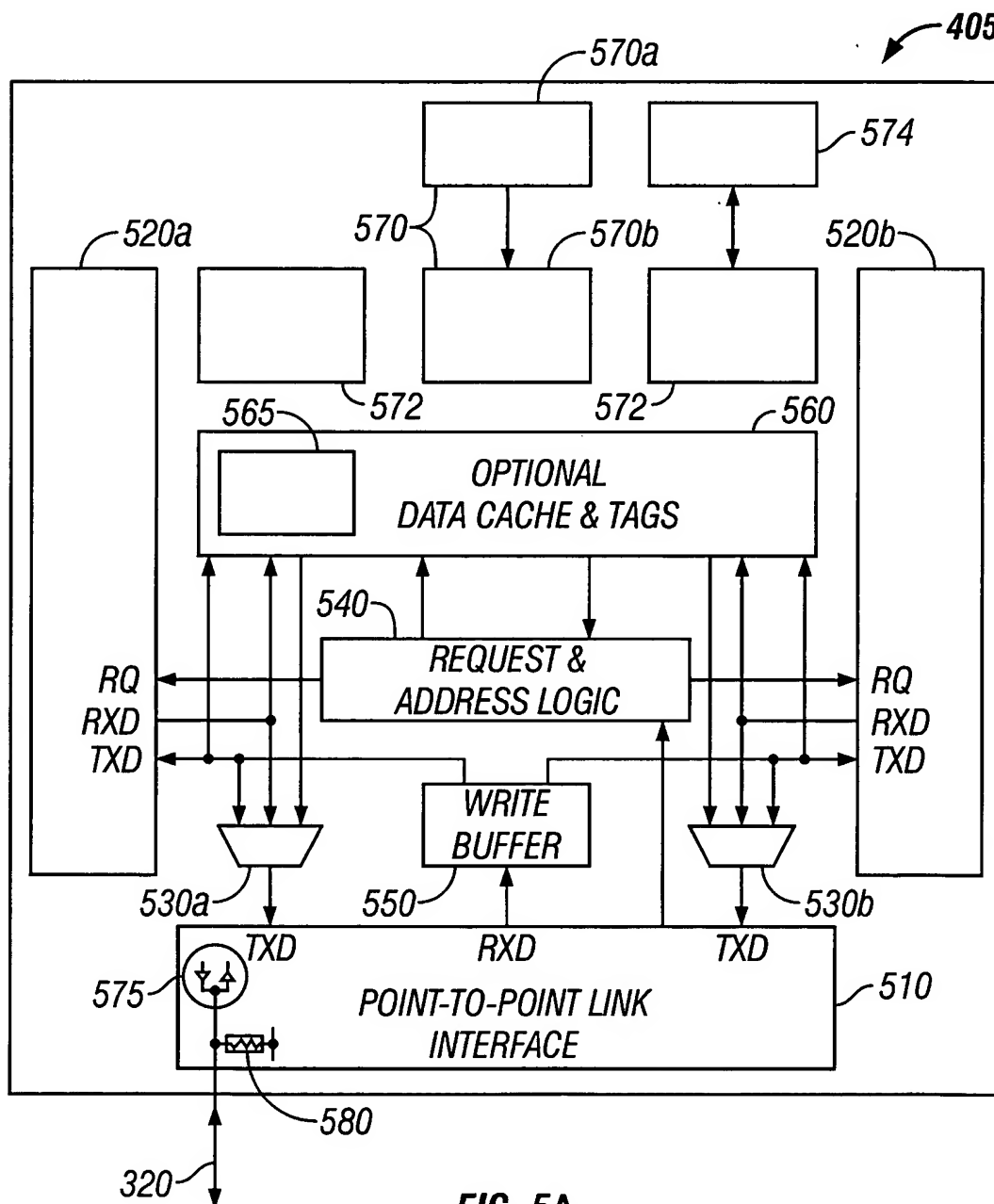


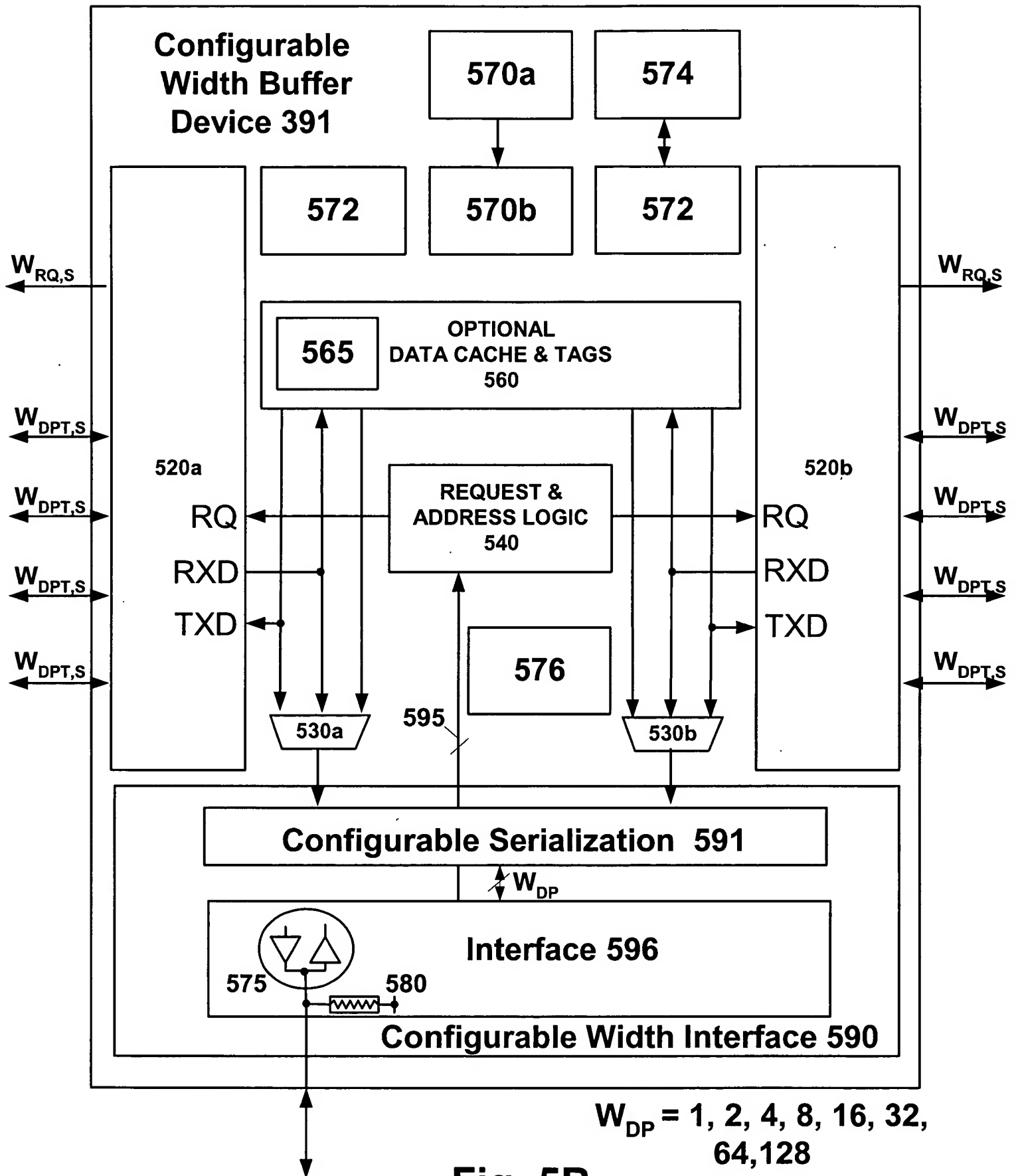
FIG. 4B



**FIG. 4C**



**FIG. 5A**



**Fig. 5B**



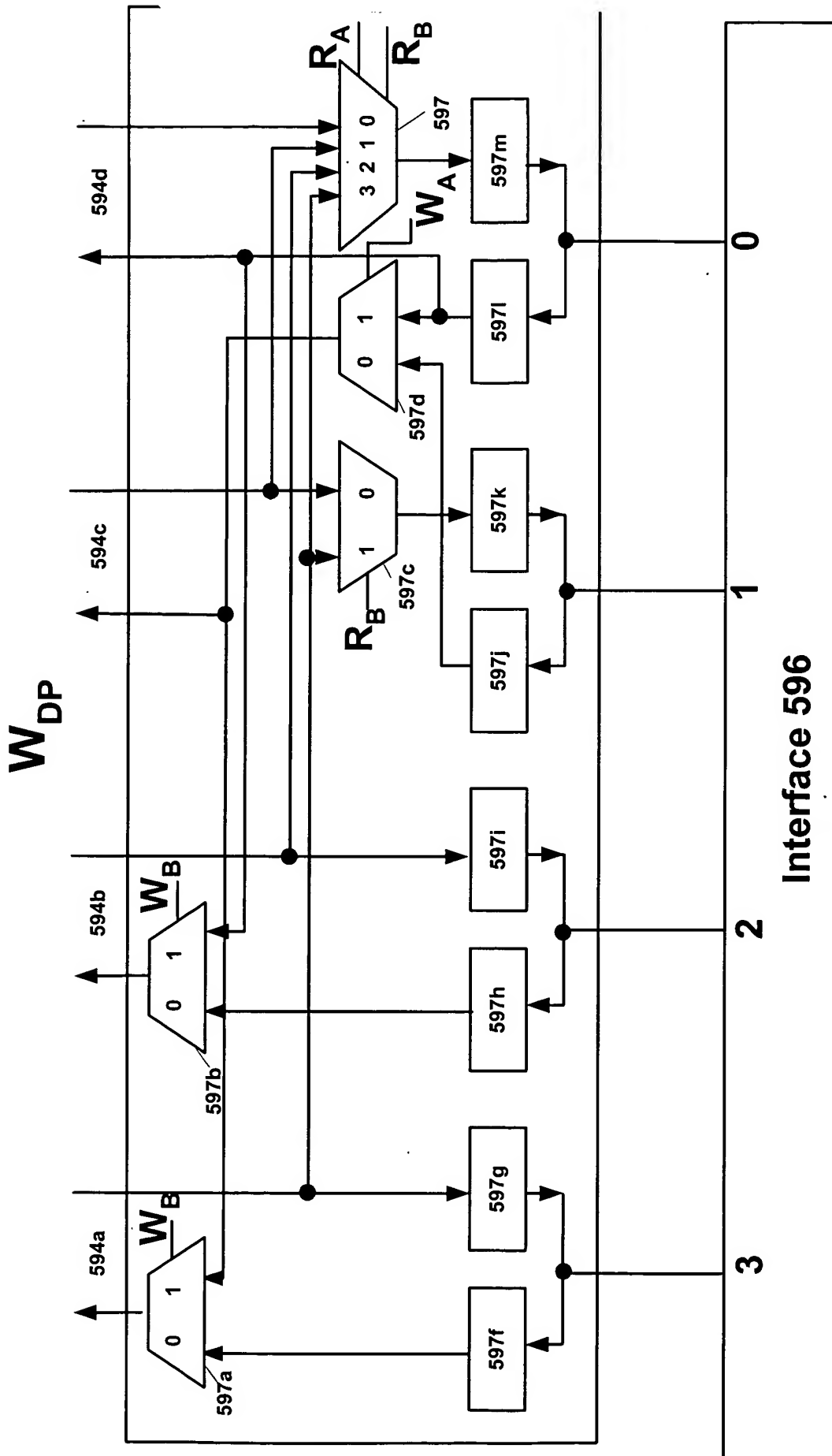


Fig. 5C

Title: CONFIGURABLE WIDTH BUFFERED MODULE

Applicants: Richard Perego et al.

Docket: RAMB-01049US2

Appl. No.: Unknown

Atty: Kirk J. DeNiro, Esq.

Filing Date: Herewith

Phone: (415) 369-9660

Express Mail No.: EV 391867374 US

Width	Write		Read		Interface 596 Connections
	$W_A$	$W_B$	$R_A$	$R_B$	
1	1	1	$A_0$	$A_1$	0
2	0	1	0	$A_0$	0 and 1
4	0	0	0	0	3, 2, 1 and 0

Fig. 5D

# Configurable Width Buffered Module 650

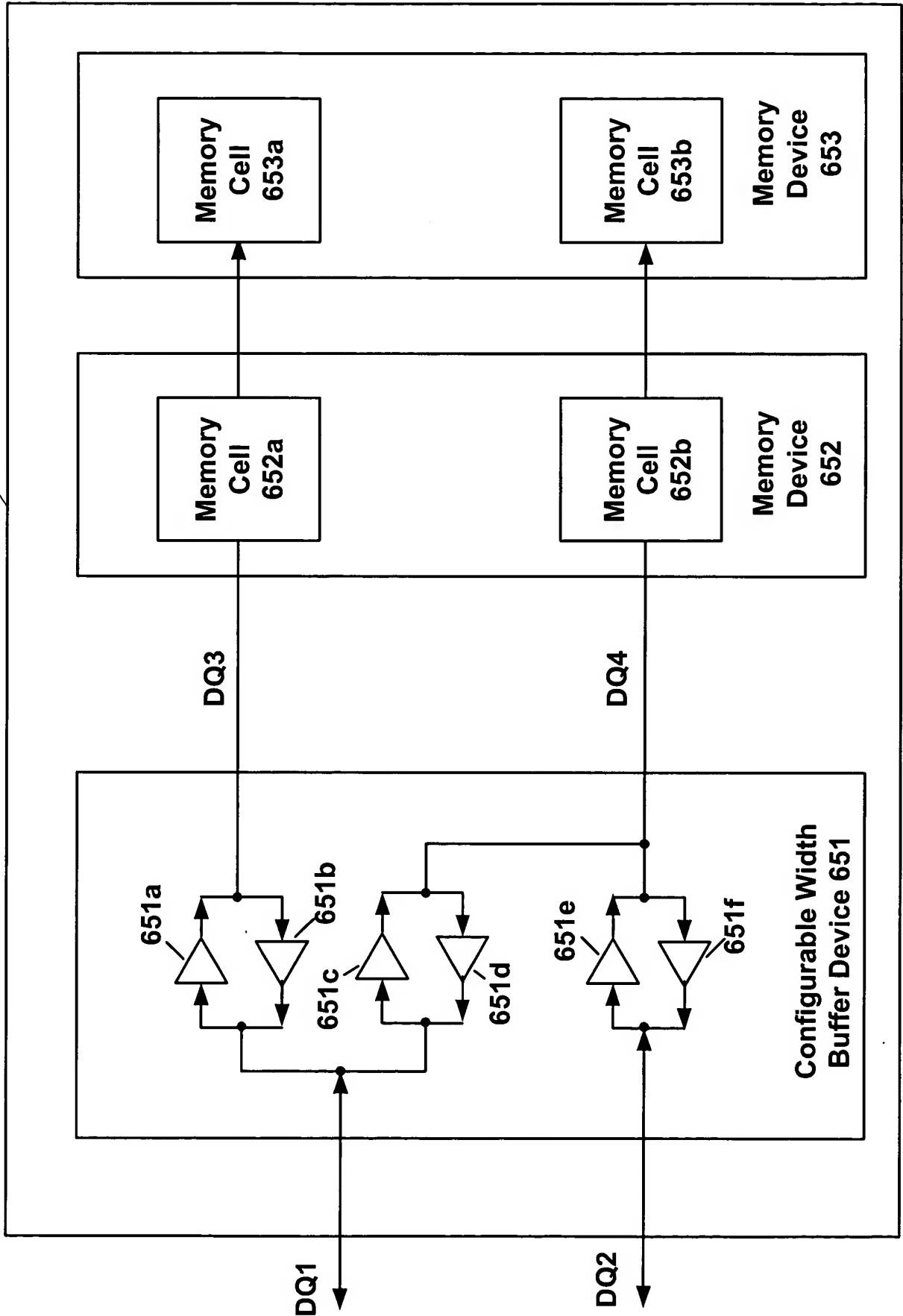


Fig. 5E

# Configurable Width Buffered Module 660

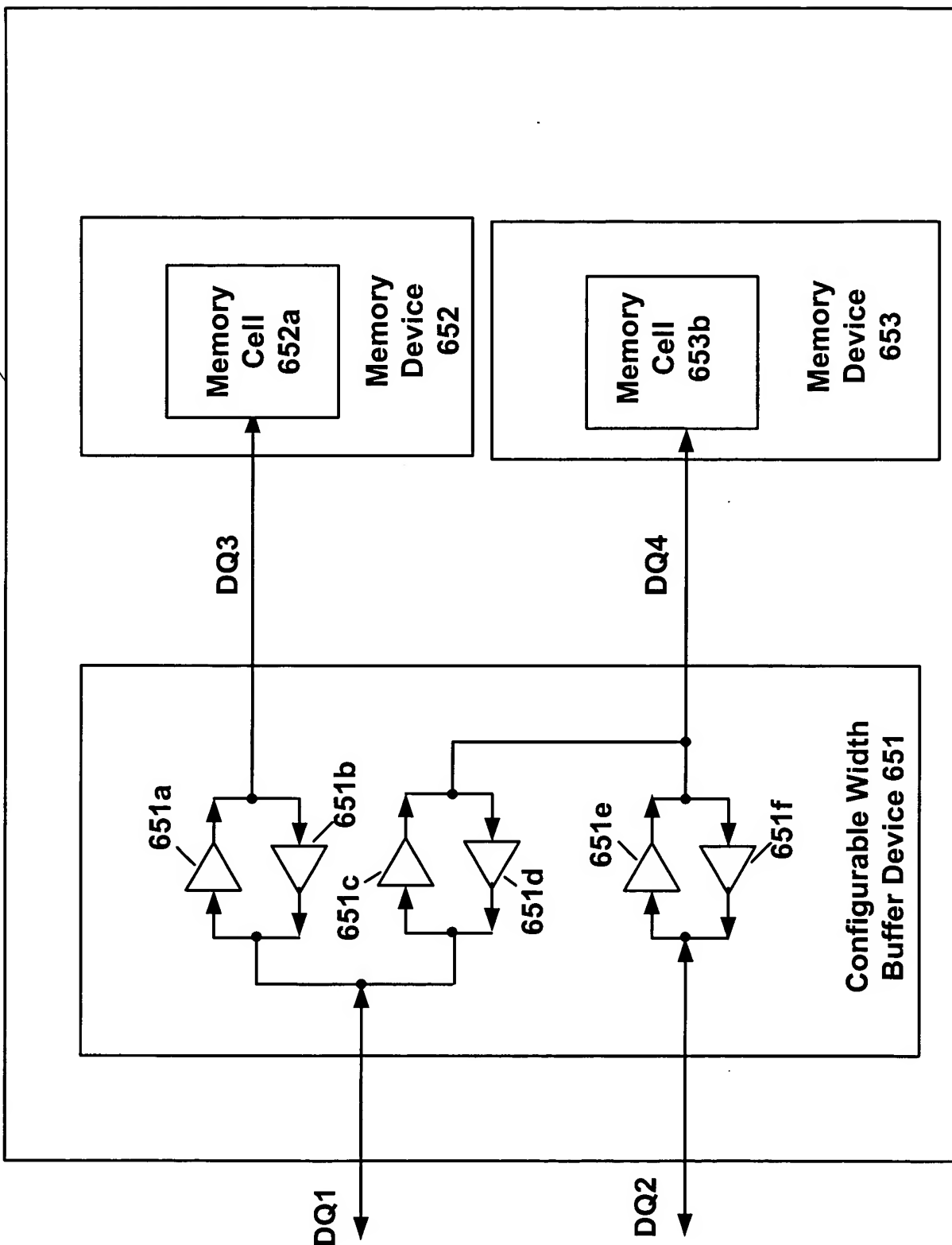
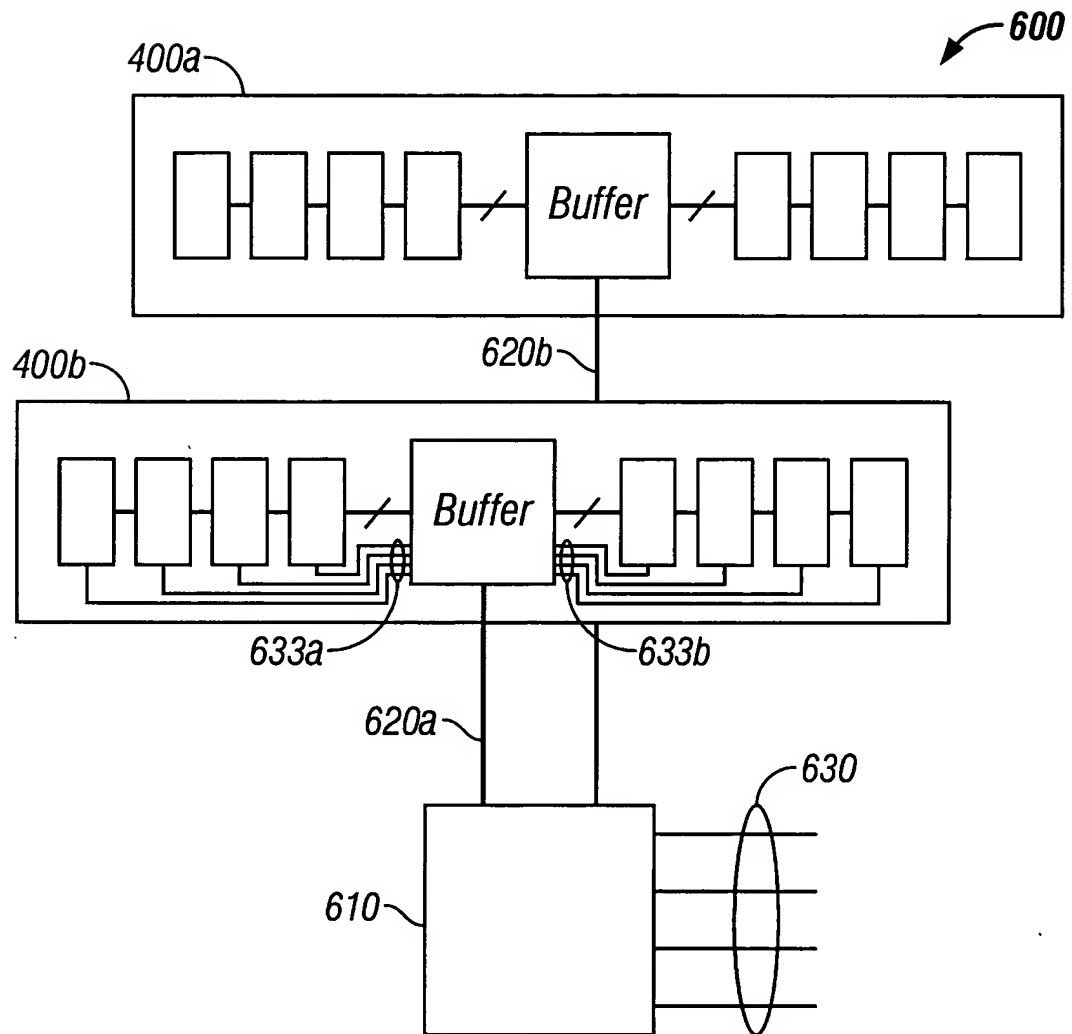
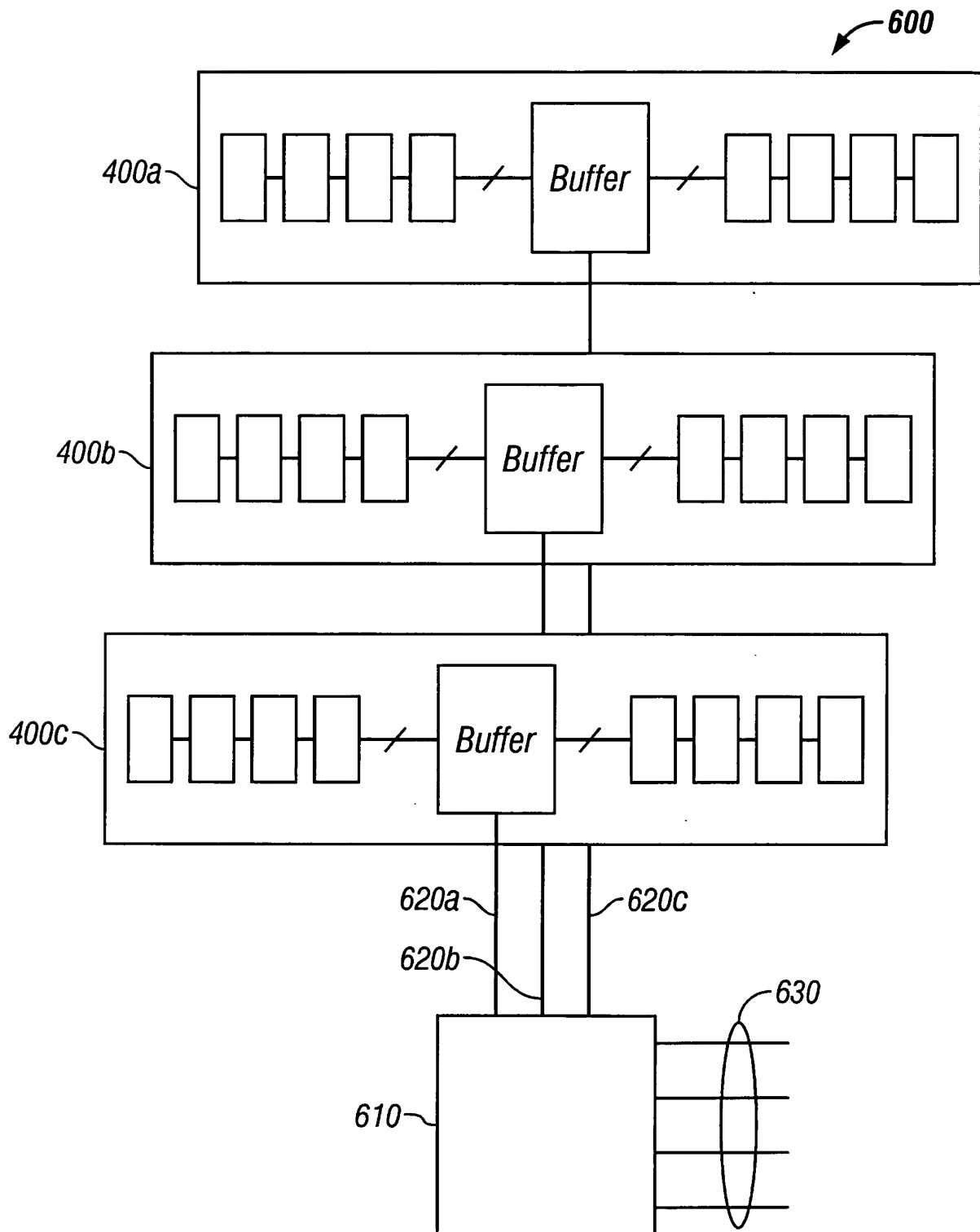
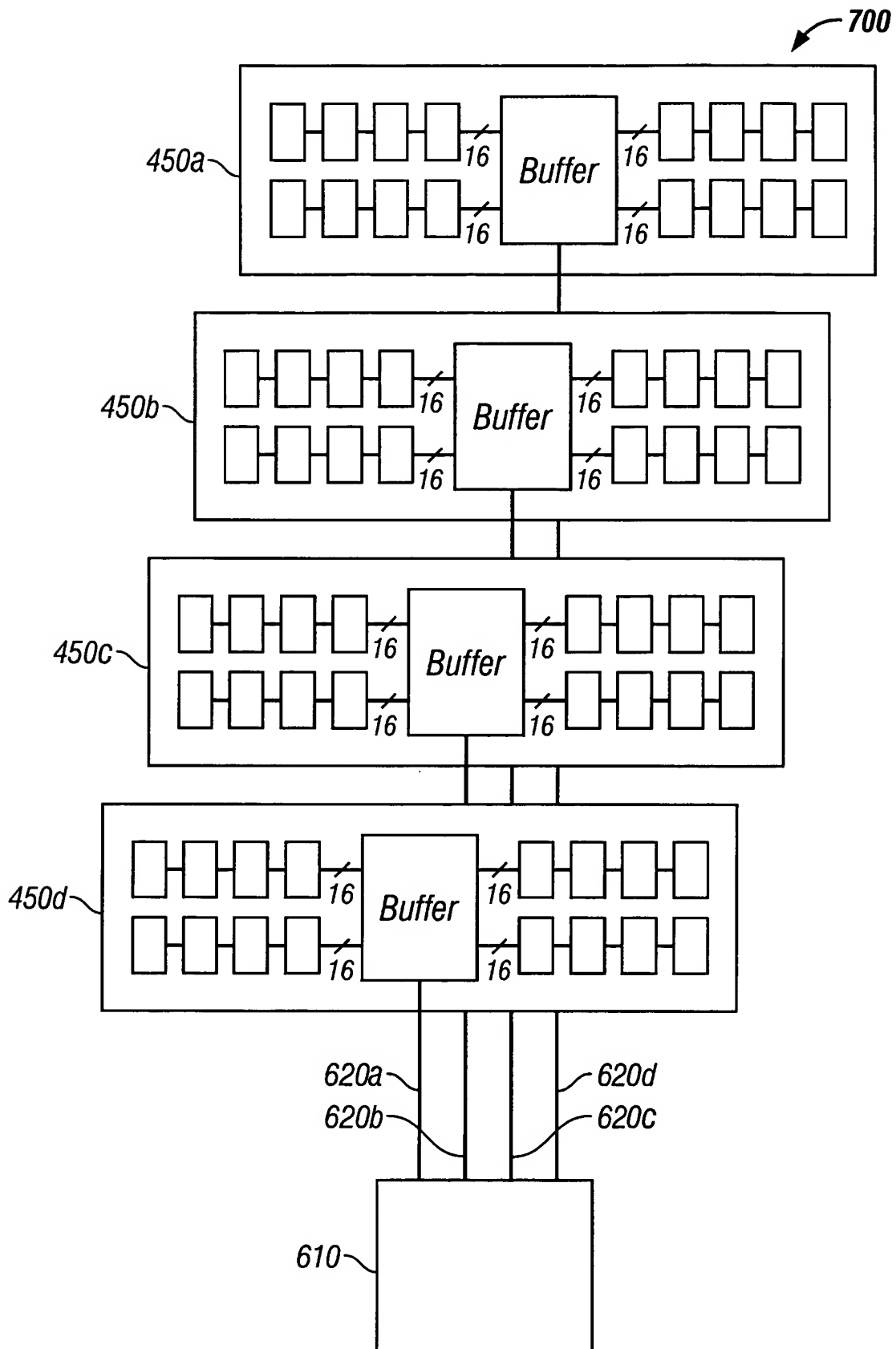


Fig. 5F

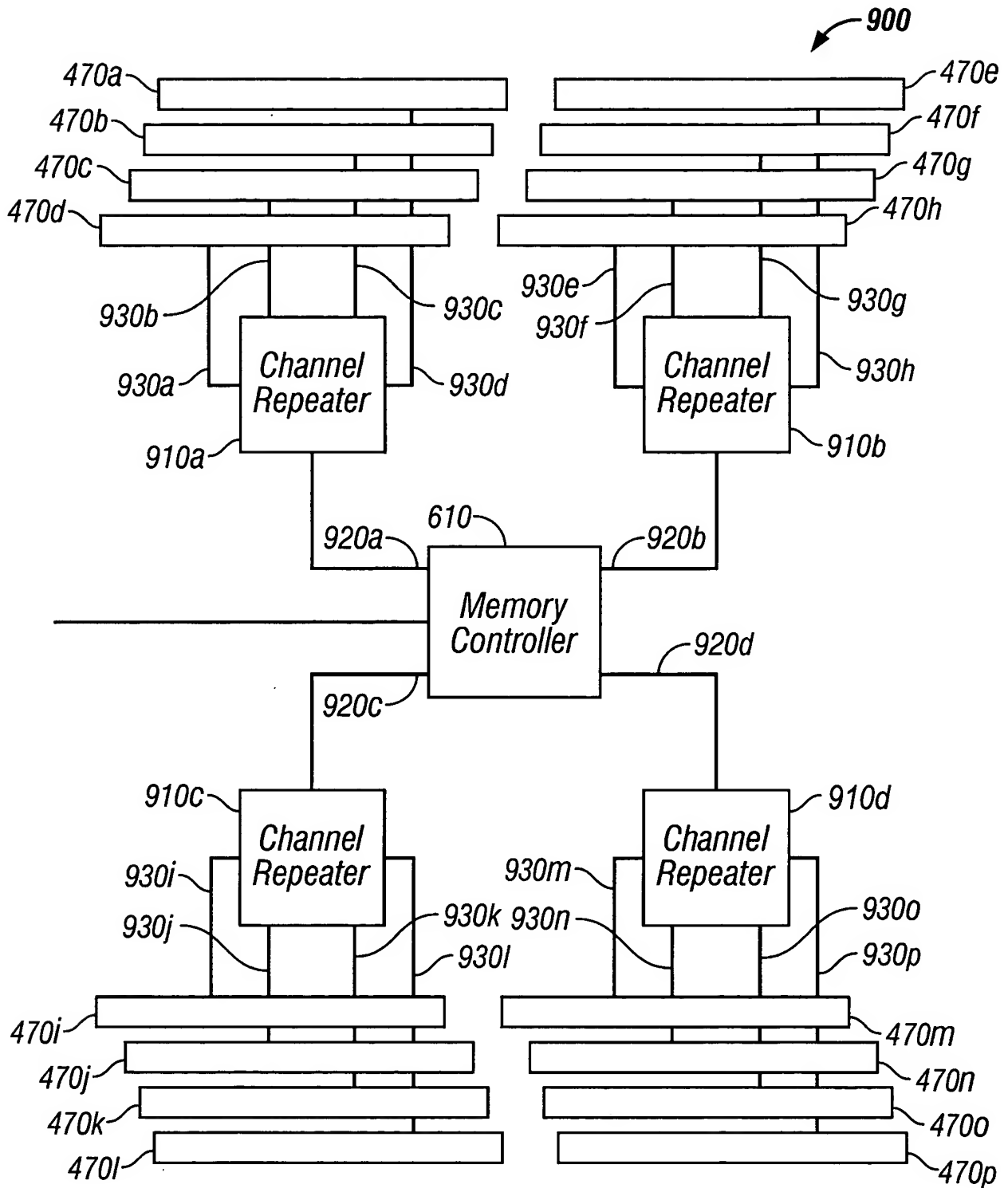


**FIG. 6A**

**FIG. 6B**

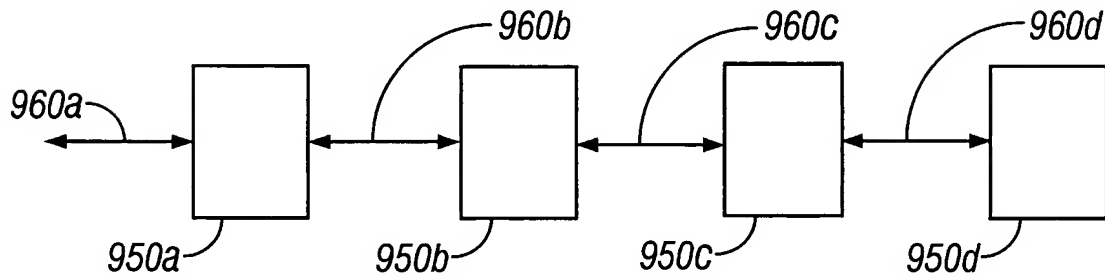


**FIG. 7**

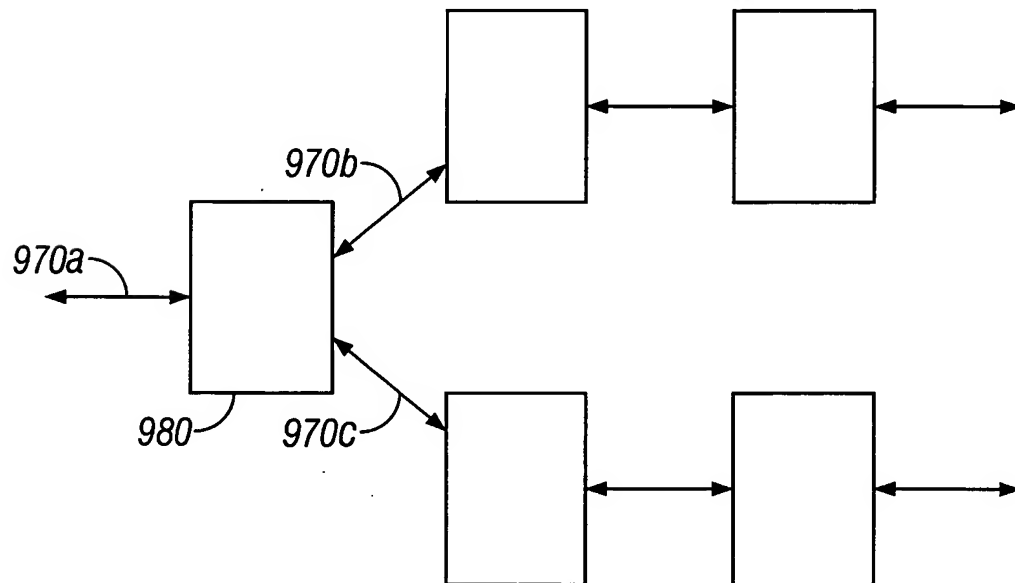


**FIG. 8A**





**FIG. 8B**



**FIG. 8C**